IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Won et al.

Serial No.: To be assigned Filed: Currently herewith

For: INTEGRATED CIRCUIT DEVICES INCLUDING A RESISTOR PATTERN

Date: September 25, 2003

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Dear Sirs:

Prior to the examination of the above-referenced application, please enter the following amendments below. Applicants provide the present Preliminary Amendment pursuant to the rules stated in revised 37 C.F.R. 1.121 that became effective on July 30, 2003.

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In the Title:

At the top of pages 1 and 21, please amend the title to read as follows:

INTEGRATED CIRCUIT DEVICES INCLUDING A RESISTOR PATTERN AND METHODS FOR MANUFACTURING THE SAME